

REPLACEMENT SHEET

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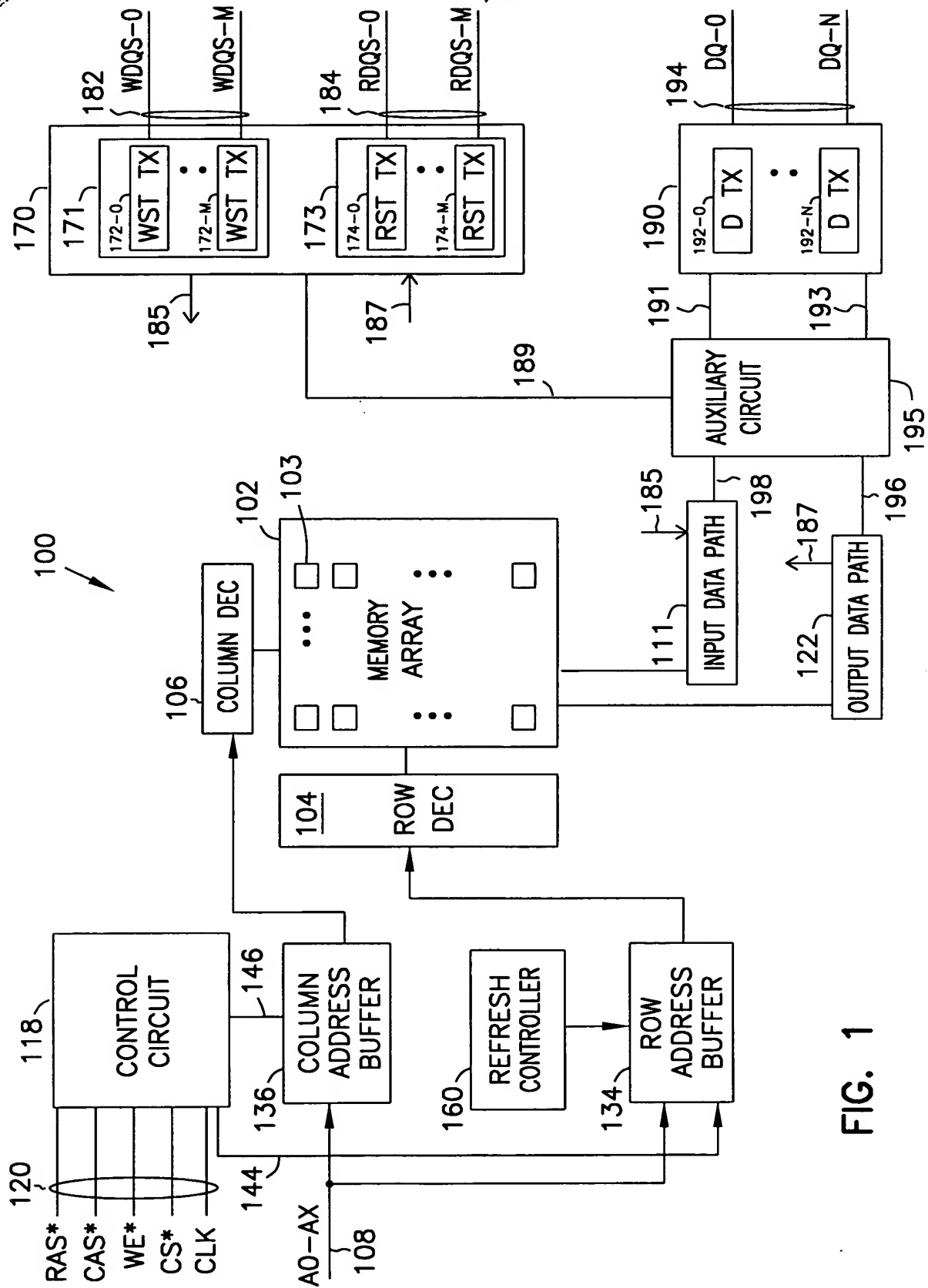


FIG. 1

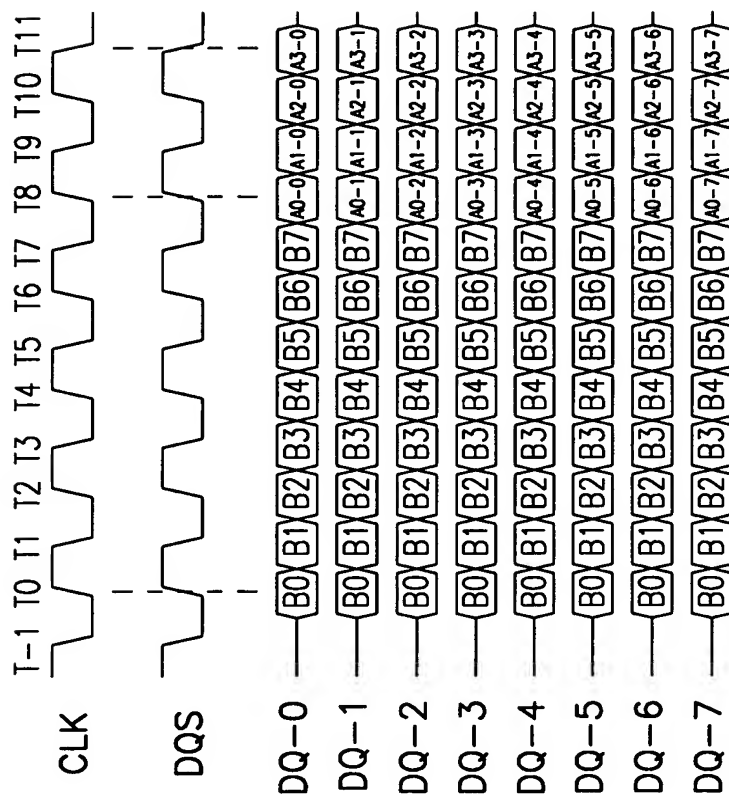
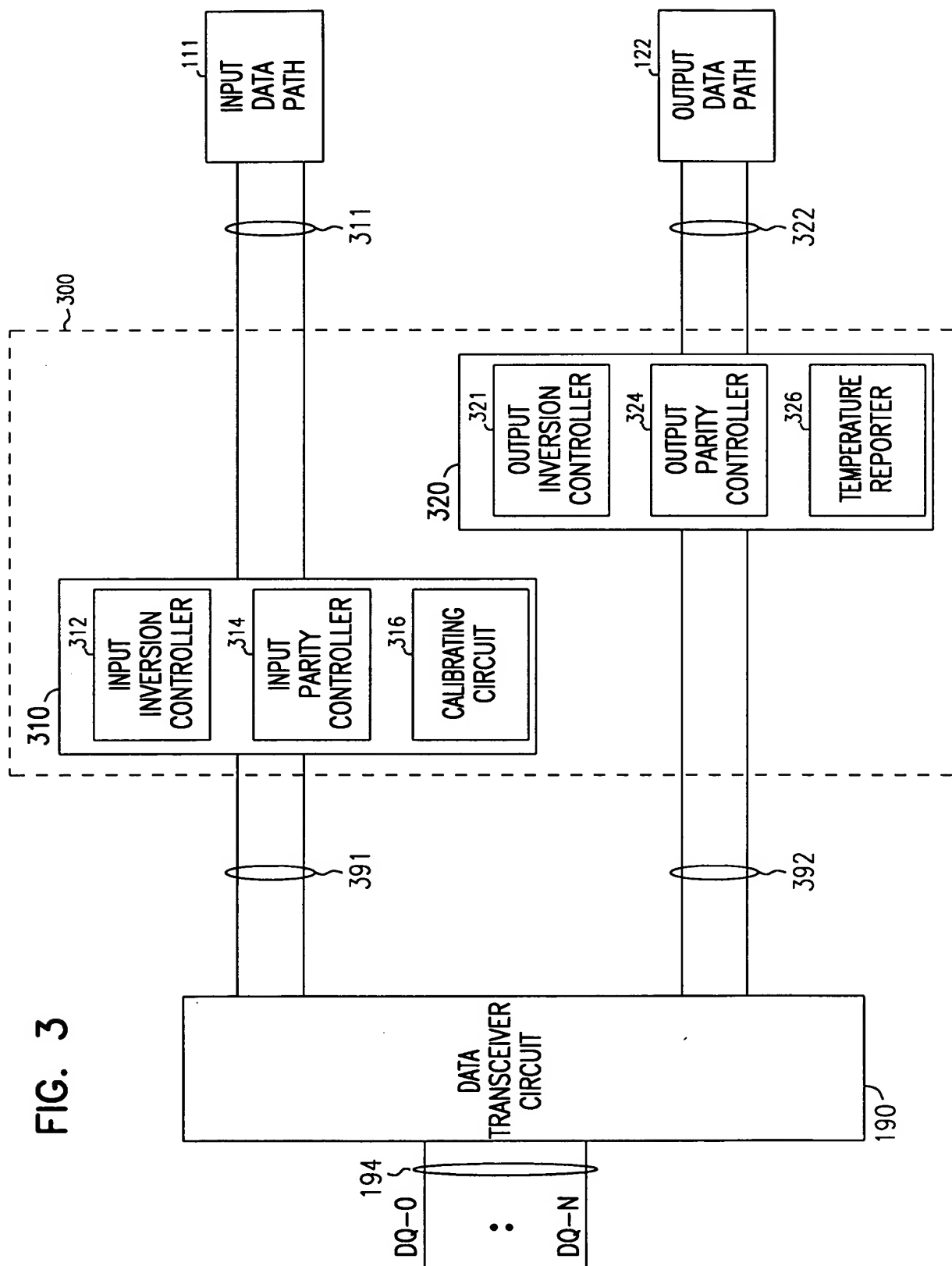


FIG. 2

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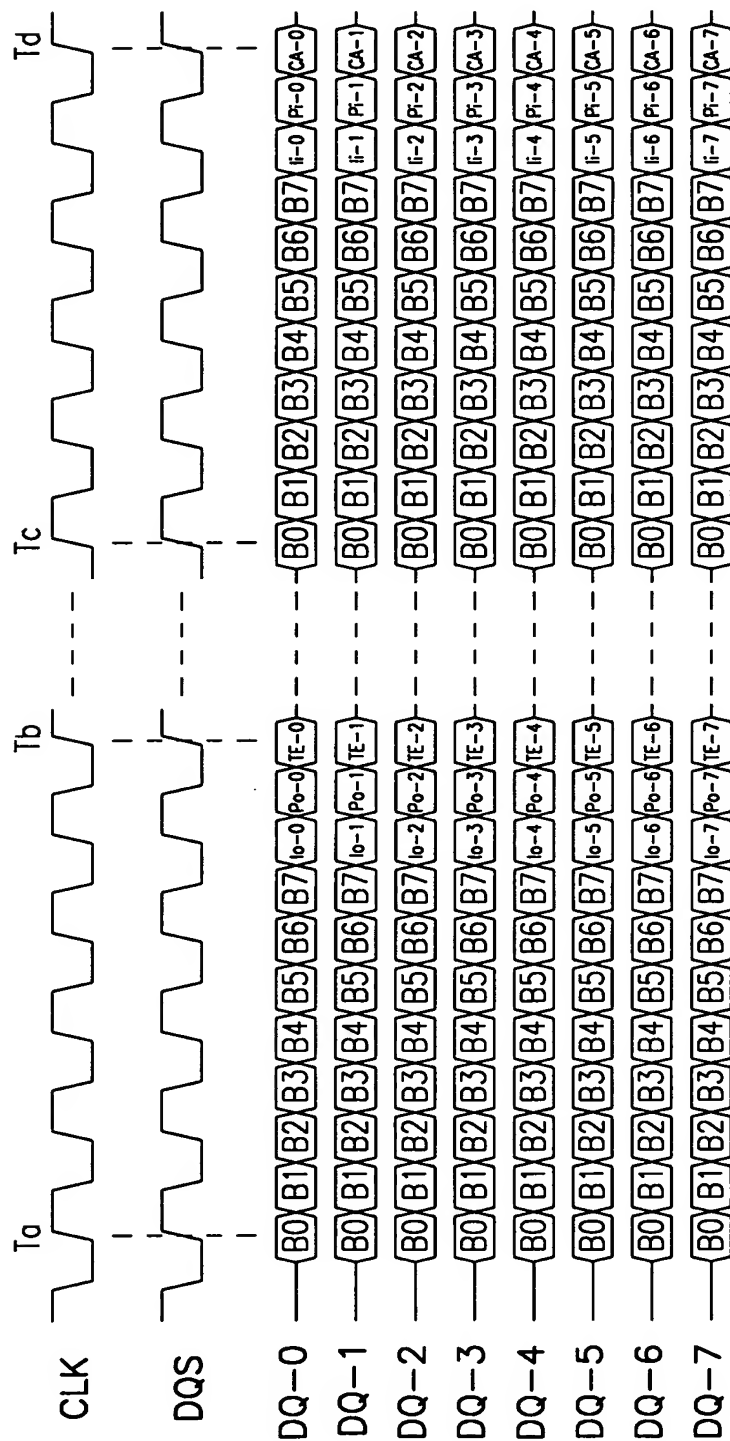


FIG. 4

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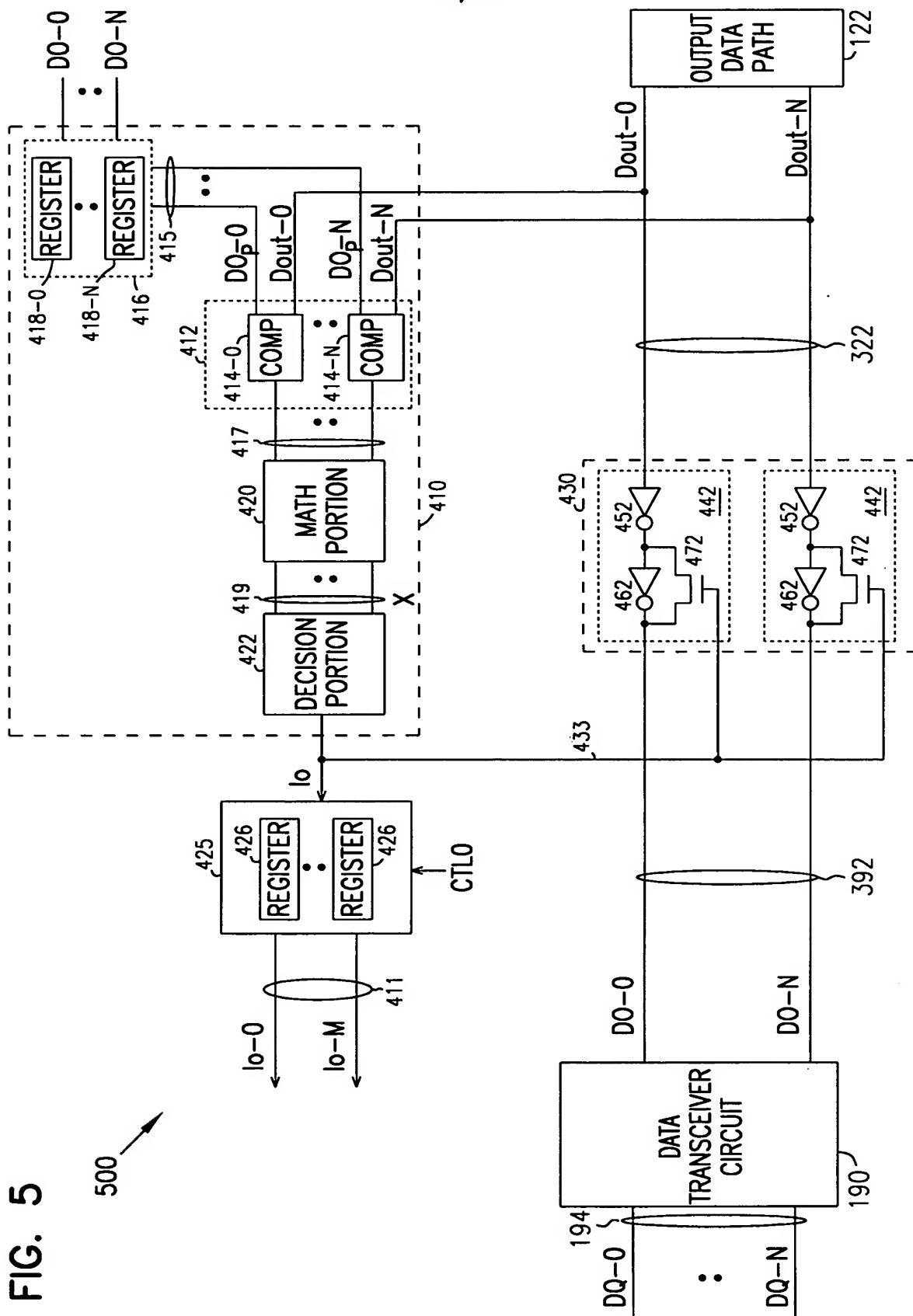


FIG. 5

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		80 80 80 80 80 80 80 80			
DO _P		00000111	10	X	Y
ROW 1 {	Dout-0 TO Dout-7 INVERTED (Dout-0 TO Dout-7)	11111000	1	8	0
		00000111			
		11110000	1	7	1
		00001111			
		11100000	1	6	2
		00011111			
ROW 6 {	Dout-0 TO Dout-7 INVERTED (Dout-0 TO Dout-7)	11000000	1	5	3
		00111111			
		10000000	0	4	4
		01111111			
		00000000	0	3	5
		11111111			
		00000001	0	2	6
		11111110			
		00000011	0	1	7
		11111100			
		00000111	0	0	8
		11111000			
		00001111	0	1	7
		11110000			
		00011111	0	2	6
		11100000			
ROW 16 {	Dout-0 TO Dout-7 INVERTED (Dout-0 TO Dout-7)	00111111	0	3	5
		11000000			
		01111111	0	4	4
		10000000			
		11111111	1	5	3
		00000000			
		11111110	1	6	2
		00000001			
		11111100	1	7	
		00000011			1

FIG. 6

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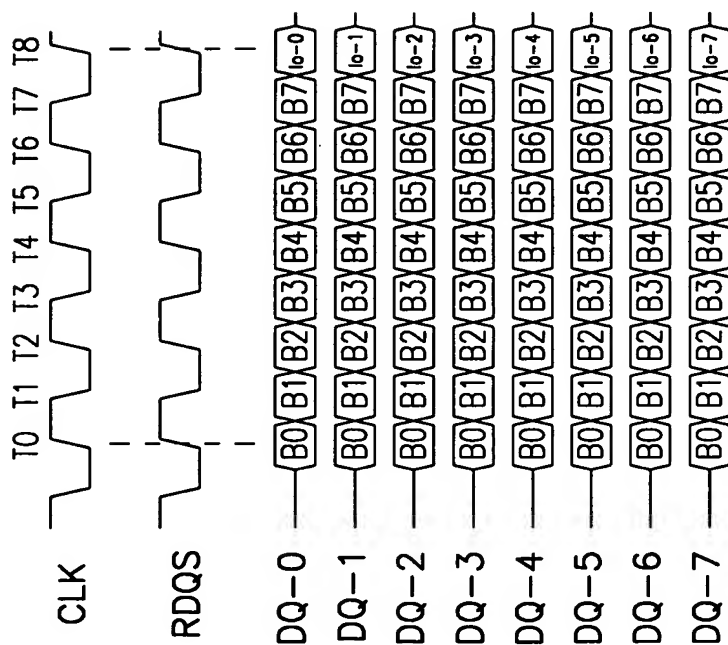
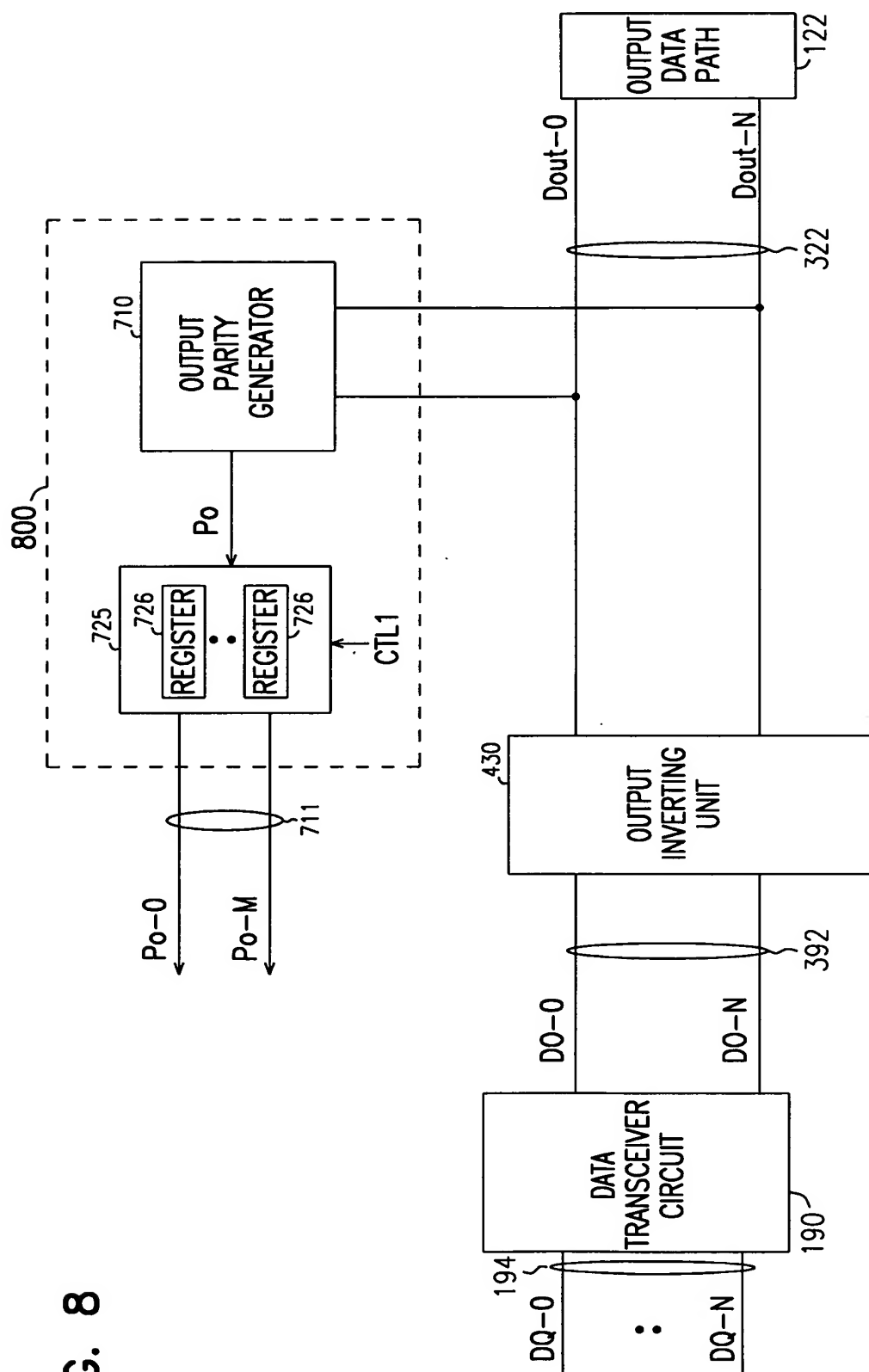


FIG. 7

FIG. 8



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	Dout-0 TO Dout-7 80 80 80 80 80 80 80 80	PO
ROW 1	00000000	0
	00000001	1
ROW 3	00000011	0
	00000111	1
	00001111	0
ROW 6	00011111	1
	00111111	0
	01111111	1
	11111111	0
	11111110	1
	11111100	0
	11111000	1
	11110000	0
ROW 14	11100000	1
	11000000	0
	10000000	1

FIG. 9

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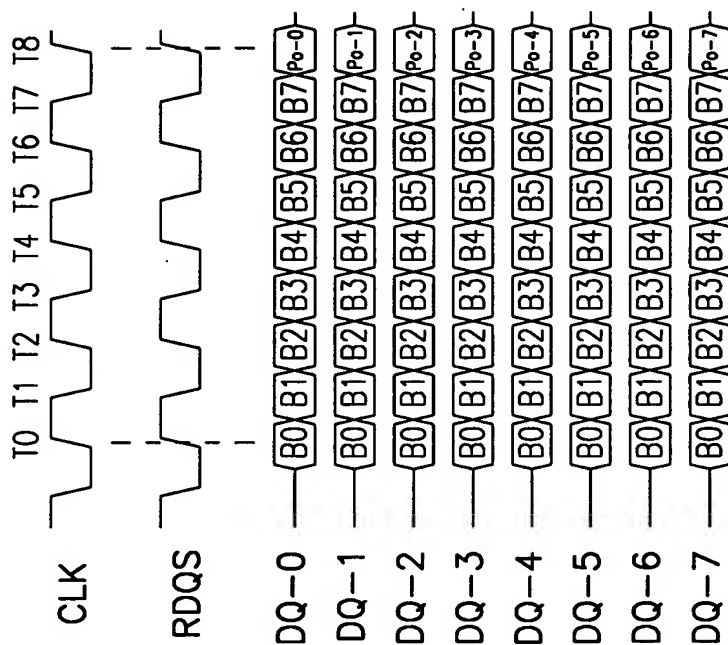
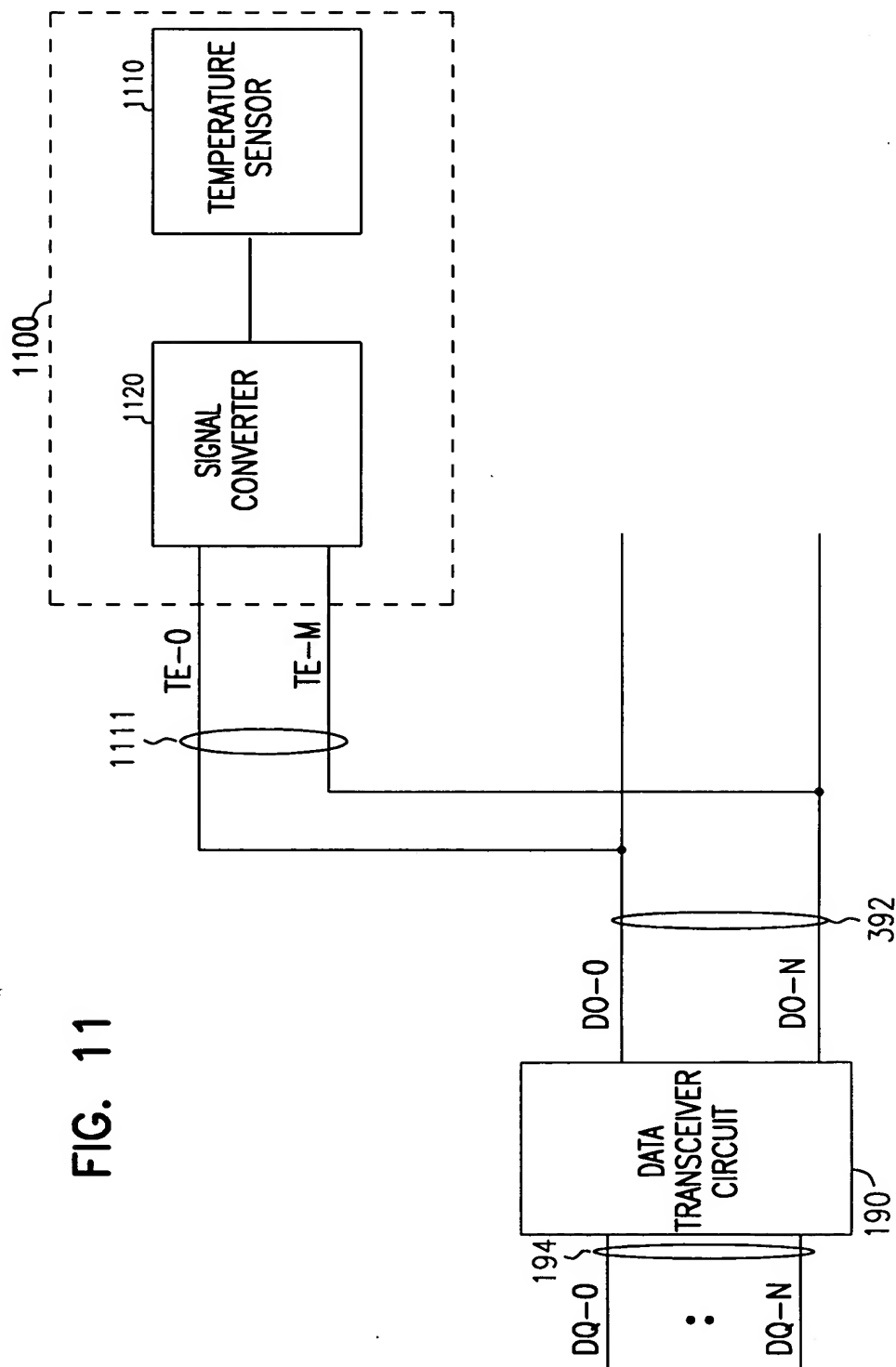


FIG. 10

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	COL1	COL2	COL3	COL4	COL5	COL6	COL7	COL8
BITS	TMP	TMP	TMP	TMP	TMP	TMP	TMP	TMP
TE-0	0	0	0	0	0	0	0	0
TE-1	0	0	0	0	0	0	0	1
TE-2	0	0	0	0	0	0	1	1
TE-3	0	0	0	0	0	1	1	1
TE-4	0	0	0	0	1	1	1	1
TE-5	0	0	0	1	1	1	1	1
TE-6	0	0	1	1	1	1	1	1
TE-7	0	1	1	1	1	1	1	1
TEMP °C	0	15	30	45	60	75	90	105

FIG. 12

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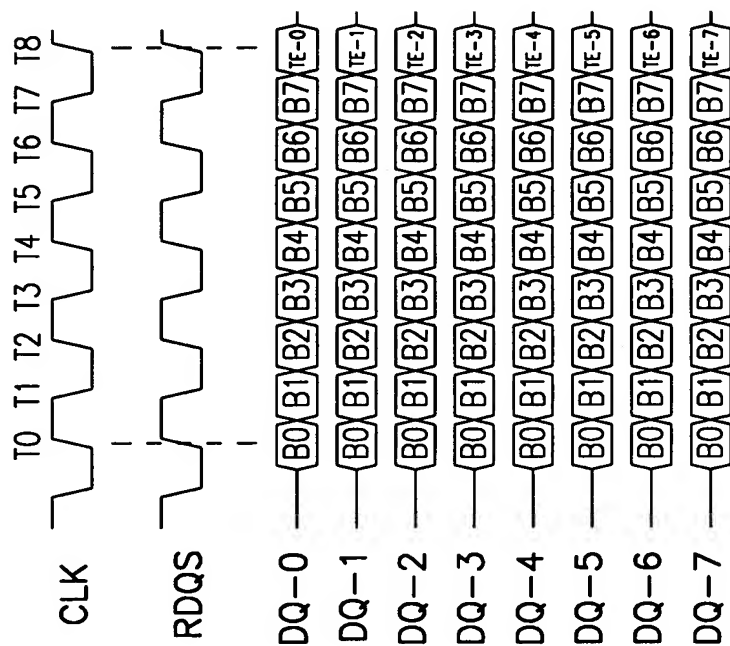


FIG. 13

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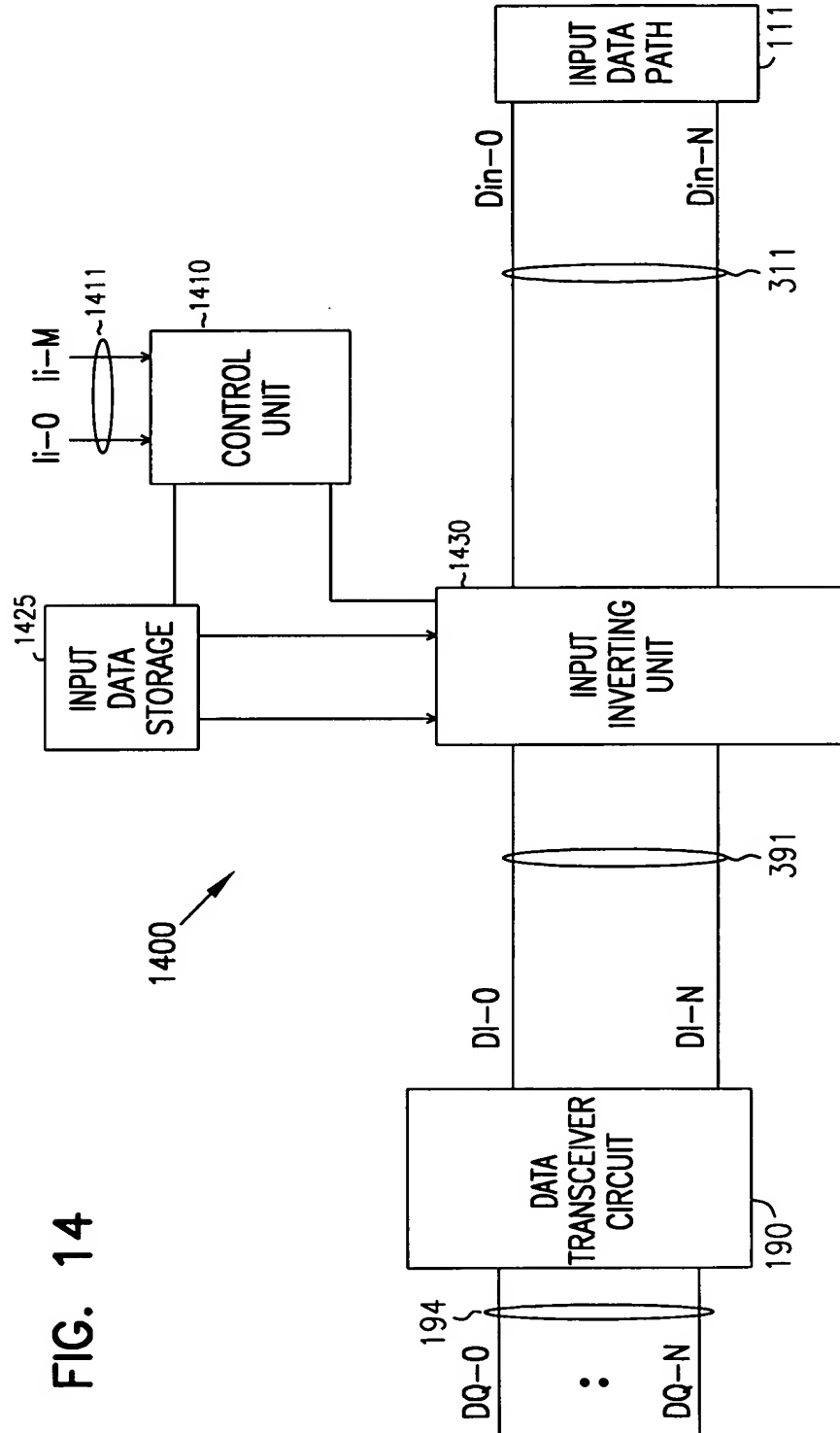


FIG. 14

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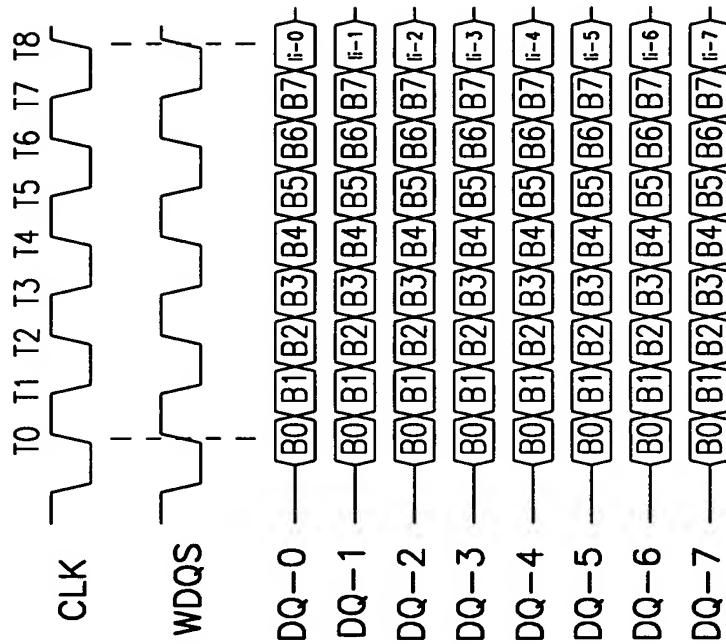


FIG. 15

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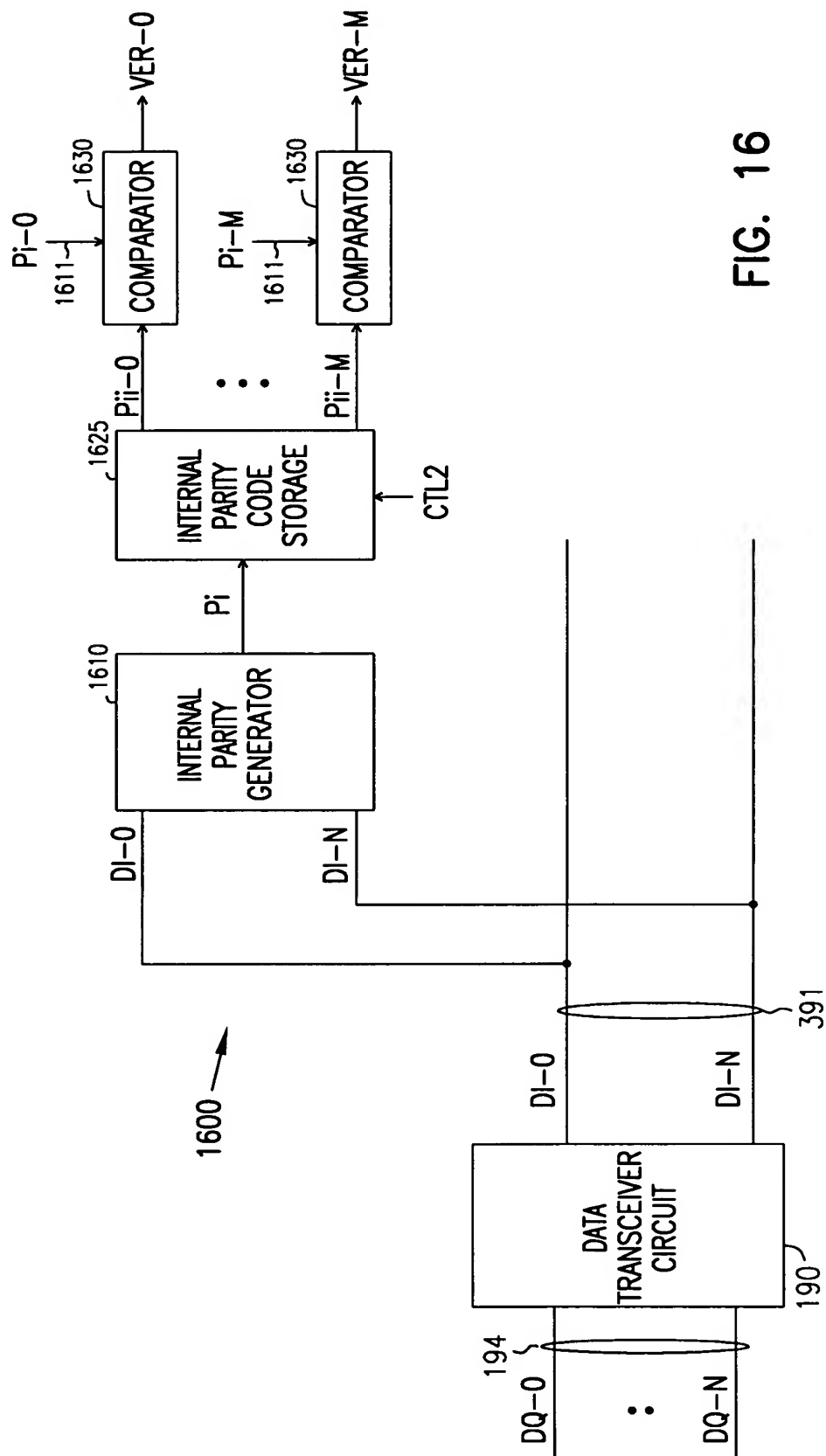


FIG. 16

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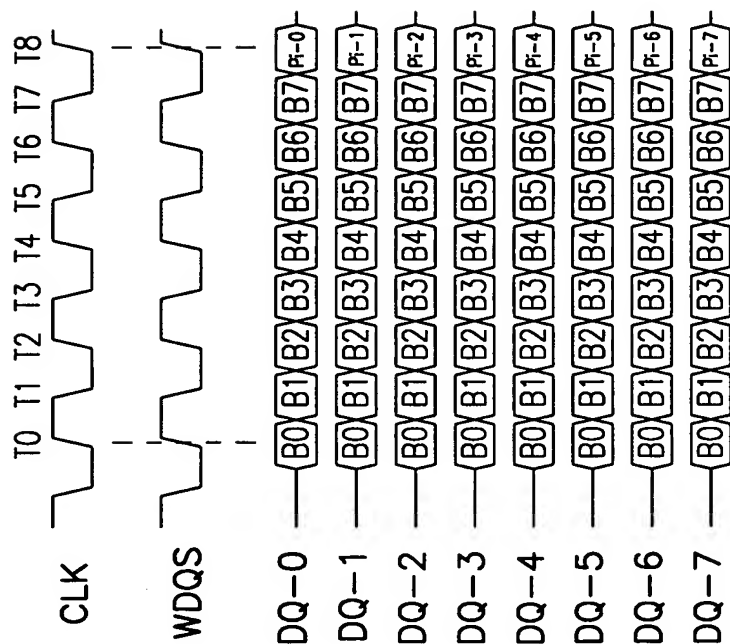


FIG. 17

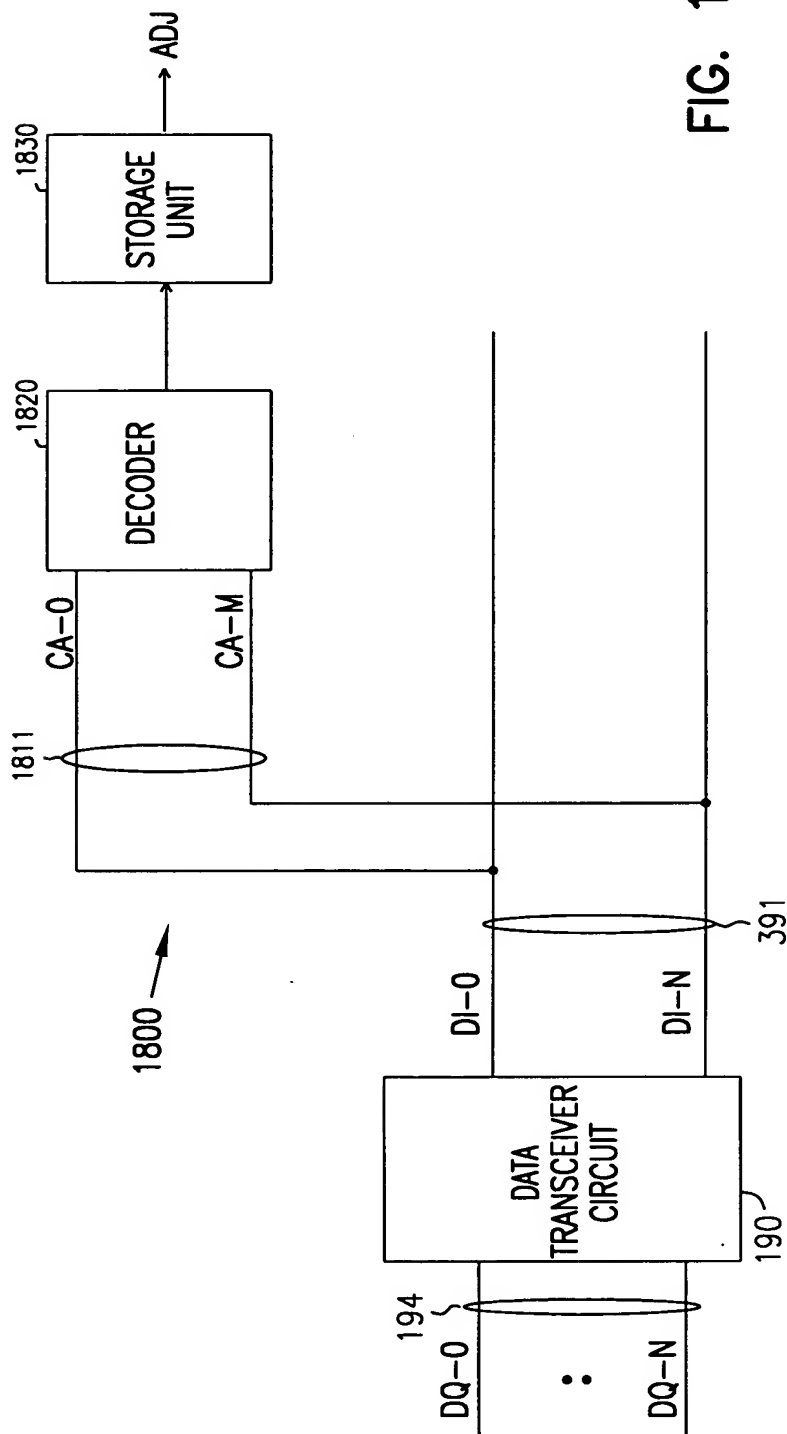


FIG. 18

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	COL1	COL2	COL3	COL4	COL5	COL6	COL7	COL8
BITS	CAL	CAL	CAL	CAL	CAL	CAL	CAL	CAL
CA-0	0	0	0	0	0	0	0	0
CA-1	0	0	0	0	0	0	0	1
CA-2	0	0	0	0	0	0	1	1
CA-3	0	0	0	0	0	1	1	1
CA-4	0	0	0	0	1	1	1	1
CA-5	0	0	0	1	1	1	1	1
CA-6	0	0	1	1	1	1	1	1
CA-7	0	1	1	1	1	1	1	1
DELAY	-200	-150	-100	-50	50	100	150	200

FIG. 19

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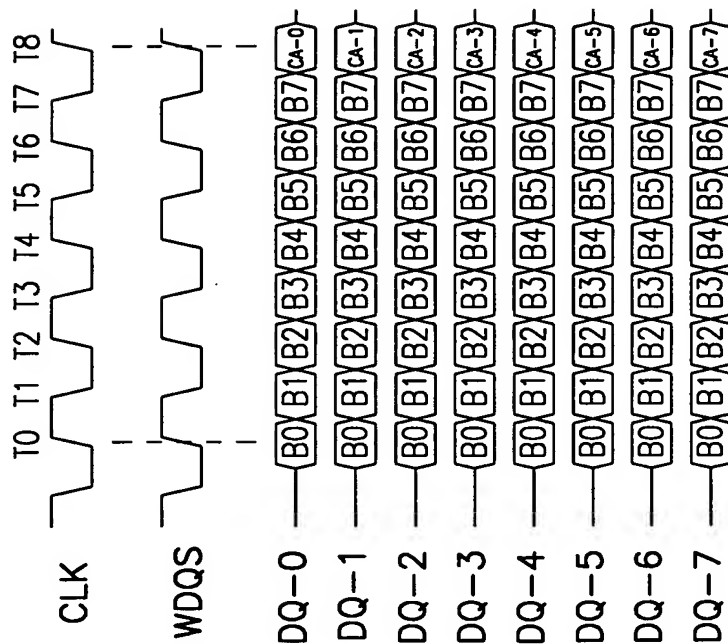


FIG. 20

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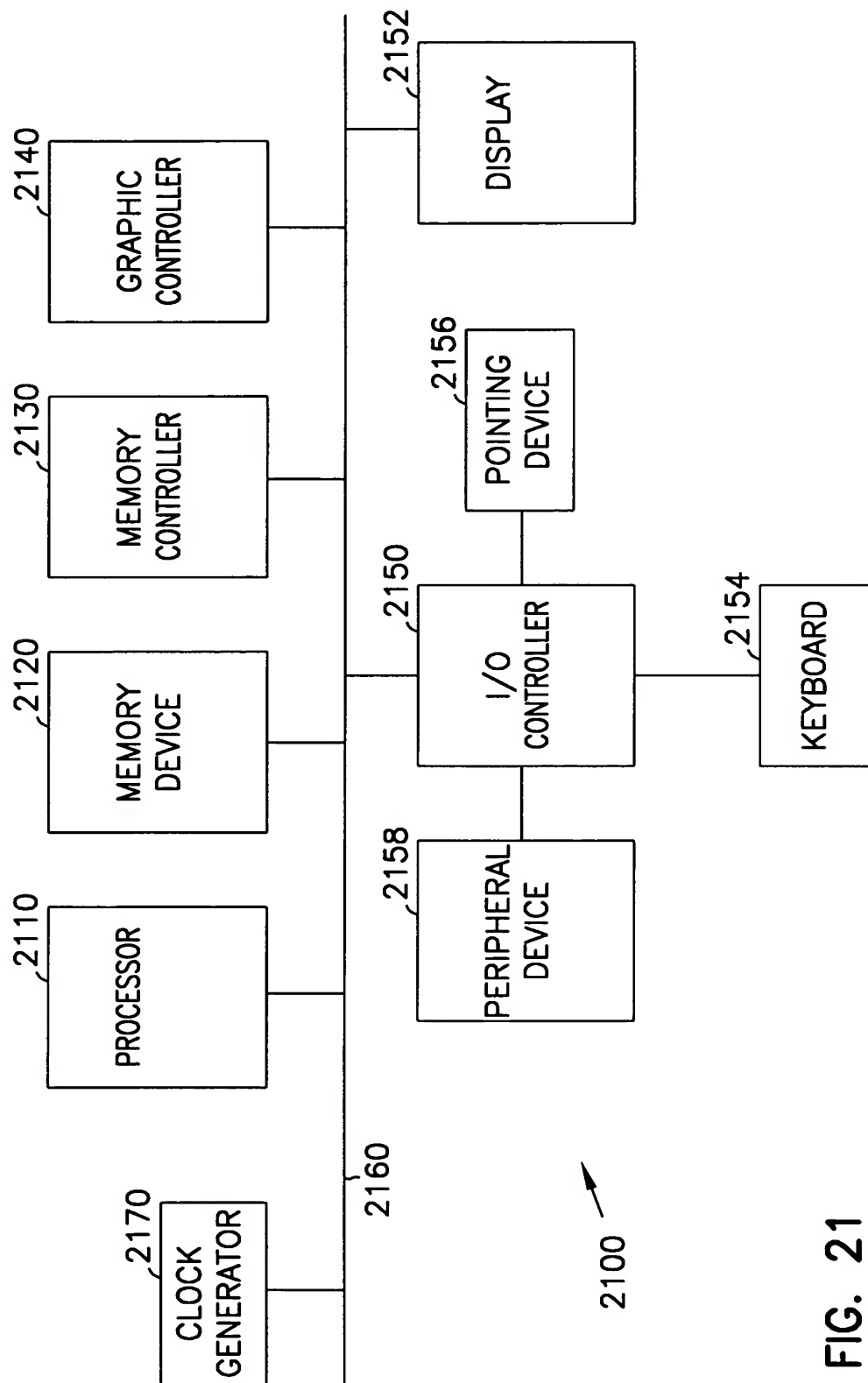


FIG. 21